

GROUP

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		E. Arai et al., "A 64-kbit Dynamic MOS RAM", IEEE Journal of Solid-State Circuits, Vol. SC-13, No. 3, June 1978, pp. 333-338.

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.